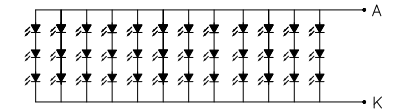
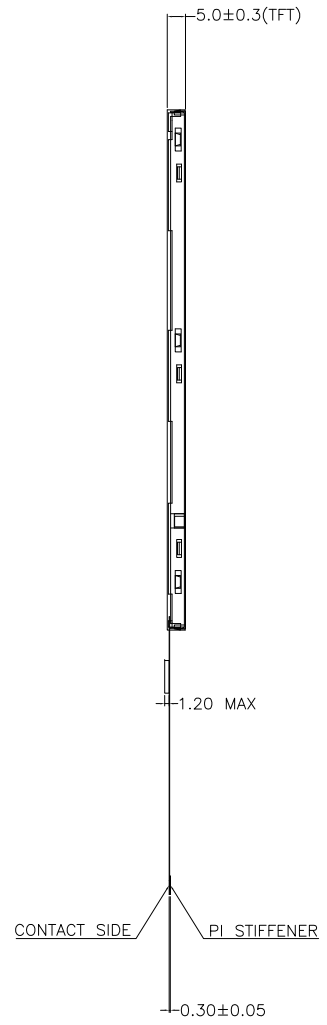


DETAIL A  
(SCALE 50:1)



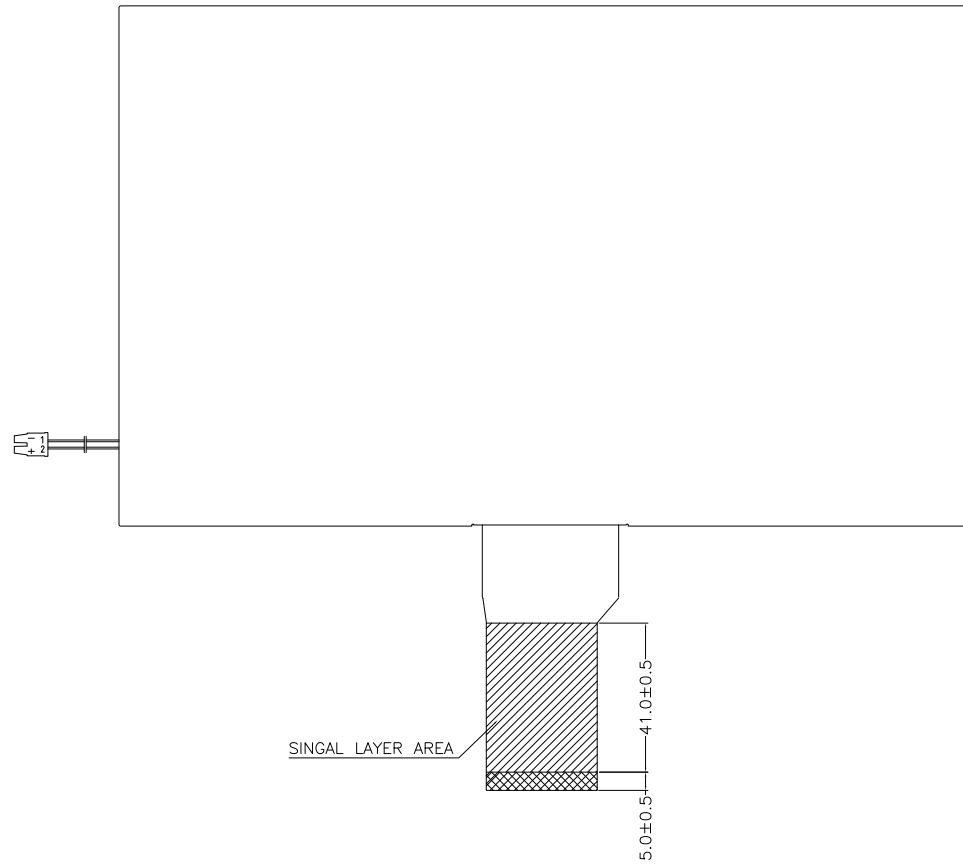
BACKLIGHT CIRCUIT DIAGRAM

PIN ASSIGNMENT			
PIN	SYMBOL	PIN	SYMBOL
1	AGND	31	NIND0
2	AVDD	32	GND
3	DVDD	33	GND
4	GND	34	GRB
5	VCOM	35	STBYB
6	DVDD	36	SHLR
7	GND	37	DVDD
8	NC	38	UPDN
9	NC	39	AGND
10	NC	40	AVDD
11	NC	41	VCOM
12	NC	42	DITH
13	NC	43	GND
14	NC	44	DVDD
15	GND	45	GND
16	DVDD	46	NC
17	GND	47	NC
18	PIND3	48	NC
19	NIND3	49	NC
20	GND	50	NC
21	PINC	51	NC
22	NINC	52	NC
23	GND	53	GND
24	PIND2	54	DVDD
25	NIND2	55	GND
26	GND	56	VGH
27	PIND1	57	DVDD
28	NIND1	58	VGL
29	GND	59	GND
30	PIND0	60	NC

- NOTES:
1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
  2. OPERATING VOLTAGE: DVDD=3.3V
  3. VIEWING DIRECTION: FULL VIEWING ANGLE
  4. IC DRIVER: EK79001H + EK73215BCGA
  5. OPERATING TEMP.: -20°C ~ 70°C
  6. STORAGE TEMP.: -30°C ~ 80°C
  7. SURFACE LUMINANCE: 650CD/M<sup>2</sup>(TYP.)
  8. BACKLIGHT: 36-CHIP WHITE LED, If=480mA, Vf=8.4V~10.5V
  9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
  10. GENERAL TOLERANCE: ±0.3MM
  11. ROHS COMPLIANT

MULTI-INNO TECHNOLOGY CO., LTD.

01	FIRST ISSUE	RITA	2019.07.25		MODULE P/N:
VER.	REVISED DESCRIPTION	REVISER	DATE	APPROVED BY:	M11010AKT-1
				VERSION NO:	DESCRIPTION:
				01	TFT MODULE
				SHEET NO.: 1/2	



MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2019.07.25		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI1010AKT-1
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 2/2	TFT MODULE

VER.	REVISED DESCRIPTION	REVISER	DATE
01	FIRST ISSUE	RITA	2019.07.25